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PATENT
P17730
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	Examiner: Not Yet Assigned
M.C. Clayton)	
Serial No.: 10/787,074)	
Filed: February 19, 2004)	Art Unit: 2666
For: GENERATING TOPOLOGY)	
INFORMATION IDENTIFYING)	
DEVICES IN A NETWORK)	
TOPOLOGY)	
)	

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

In accordance with 37 CFR §§ 1.56, 1.97, and 1.98, enclosed please find Form PTO-1449 listing the attached references which might be deemed material to the examination of the above-identified application. Applicants submit that no fee is needed nor should be charged with the submittal of this Information Disclosure Statement (IDS) because this IDS is being submitted under 1.97(b) in that the IDS is being filed before the mailing date of a first Office Action on the merits or within three months of the filing date of a national application other than a continued prosecution application under 1.53(d). Applicants submit that a first Office Action has not been received in the above referenced case.

Nonetheless, should any fee be deemed necessary, please charge any insufficiency or credit any overpayment to Deposit Account No. 50-0585. A duplicate of this letter is enclosed for fee processing.

Respectfully submitted,

David W. Victor

Dated: May 25, 2005

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 25, 2005.

David W. Victor

5/25/05
(Date)



Form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

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Complete if Known

Application Number	10/787,074
Filing Date	February 19, 2004
First Named Inventor	M.C. Clayton
Art Unit	2666 2477
Examiner Name	Not Yet Assigned Yong Zhou

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number Number- Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-			
		US-			
		US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document Country Code- Number- Kind Code	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
/Y.Z./		AMERICAN NATIONAL STANDARDS INSTITUTE, "Fiber Channel Arbitrated Loop (FC-AL-2)", <i>American National Standard for Information Technology</i> , 1999, pp.i-v & 1-16.	
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/Y.Z./		DEYRING, K-P., "Serial ATA: High Speed Serialized AT Attachment", January 7, 2003, Revision 1.0a, pp.1-22.	
/Y.Z./		ELLIOT, R.C. (Ed.), "Information Technology- SCSI Enclosure Services-2 (SES-2)", <i>Project T10/1559-D</i> , July 15, 2003, Revision 4, pp. i-xiv & 1-79.	
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/Y.Z./		ELLIOT, R., "Serial Attached SCSI Architecture", September 2003, pp. 1-46.	
/Y.Z./		FAIRCHILD, S., "Expander Configuration Details", <i>T10 Document 02-359</i> , September 9, 2002, pp. 1-17.	
/Y.Z./		FAIRCHILD, S., "SAS Expander Initiator Based Configuration", July 12, 2002, pp. 1-32.	
/Y.Z./		IEEE COMPUTER SOCIETY, "Part 3: Carrier Sense Multiple Access With Collision Detection (CSMA/CS) Access Method and Physical Layer Specification", <i>IEEE Standard for Information Technology 802.3</i> , 2002, pp. 1-32.	
/Y.Z./		MCLEAN, P.T. (Ed.), "Information Technology- AT Attachment with Packet Interface-6 (ATA/ATAPI-6)", <i>T13/1410D</i> , February 26, 2002, Revision 3B, pp. 1-56.	
/Y.Z./		PCI SPECIAL INTEREST GROUP, "PCI Local Bus Specification", March 29, 2002, Revision 2.3, pp. i-xiv & 1-6.	
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/Y.Z./		QLOGIC CORPORATION, "Enclosure Management Controller for Serial ATA", <i>Gem424 Data Sheet</i> , 2001, pp. 1-4.	

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
 (use as many sheets as necessary)

				<i>Complete if Known</i>
Application Number	10/787,074			
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First Named Inventor	M.C. Clayton			
Art Unit	2666-2477			
Examiner Name	Not Yet Assigned Yong Zhou			
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				P17730

/Y.Z./	U.S. Patent Application Serial No. 10/741,460, filed December 18, 2003, entitled "Multiple Interfaces in a Storage Enclosure", invented by P.L. Seto.
/Y.Z./	U.S. Patent Application Serial No. 10/742,029, filed December 18, 2003, entitled "An Adaptor Supporting Different Protocols", invented by P.L. Seto & D.N. Atallah.
/Y.Z./	U.S. Patent Application Serial No. 10/742,030, filed December 18, 2003, entitled "Enclosure Management Device", invented by P.L. Seto.
/Y.Z./	U.S. Patent Application Serial No. 10/742,302, filed December 18, 2003, entitled "Address Assignment for Adaptor Interfaces", invented by P.L. Seto.
/Y.Z./	U.S. Patent Application Serial No. 10/746,657, filed December 23, 2003, entitled "Handling Redundant Paths Among Devices", invented by N.E. Marushak.

Examiner Signature	/Yong Zhou/	Date Considered	01/24/2010
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.